

Figure 1

Figure 2

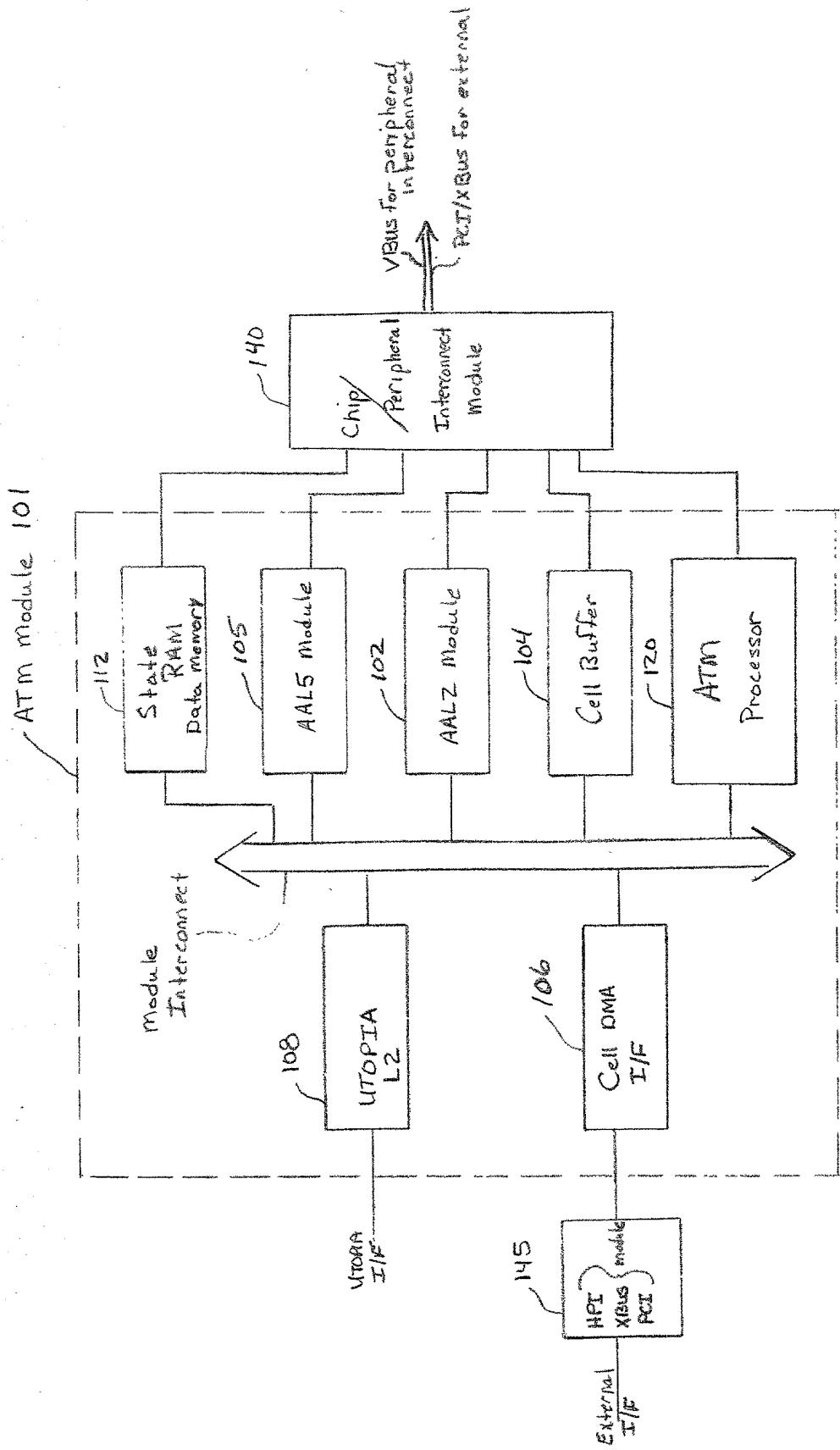
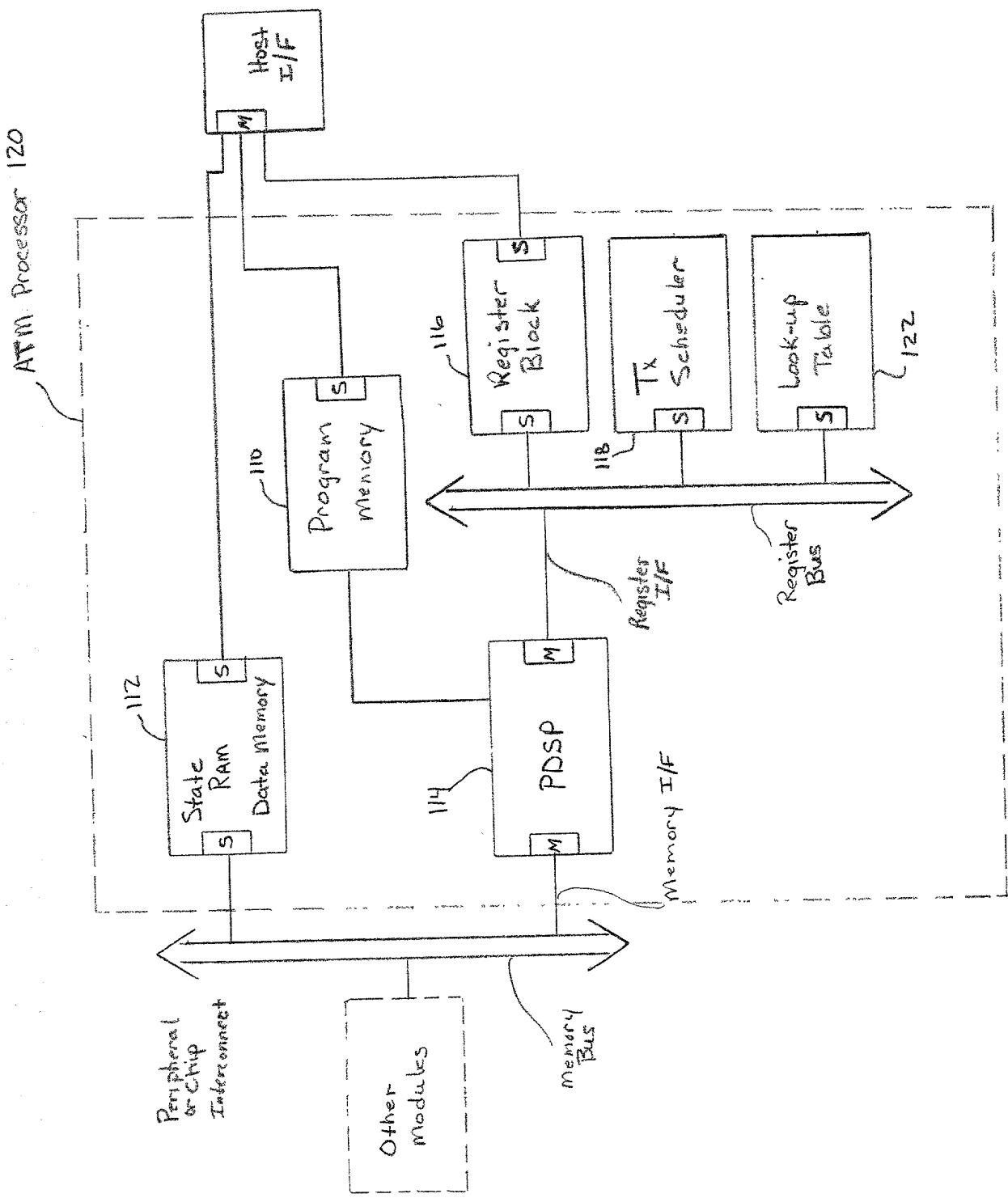
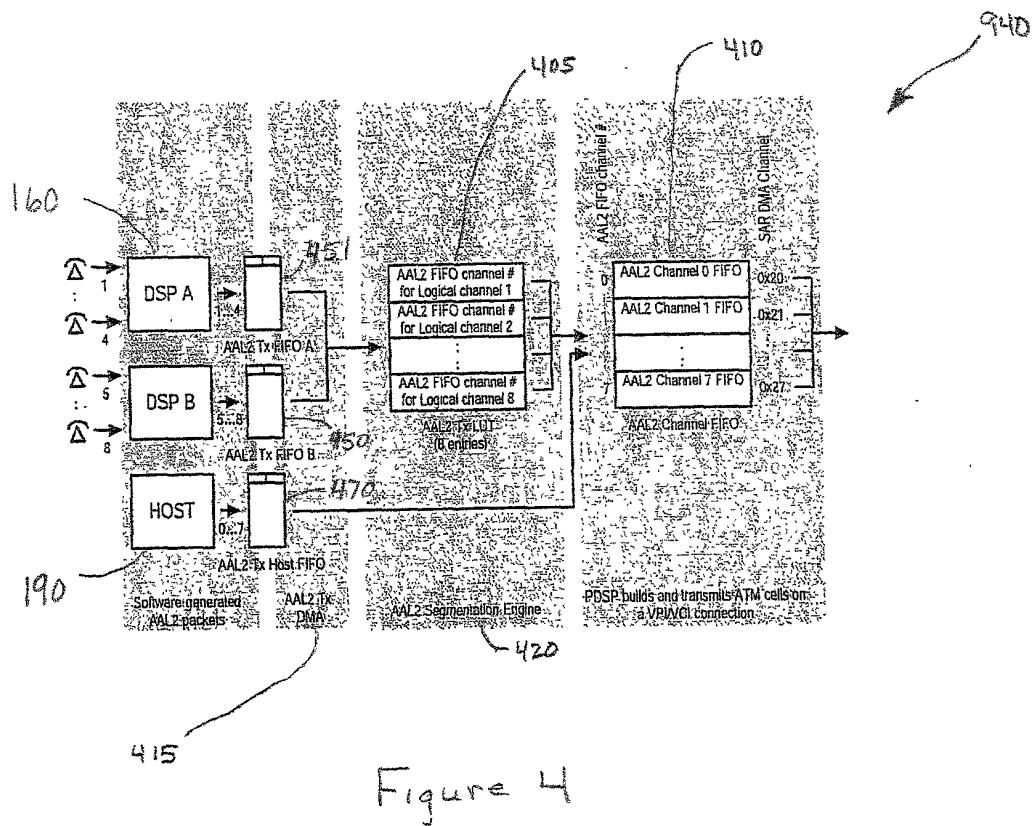


Figure 3



100 200 300 400 500 600 700 800 900



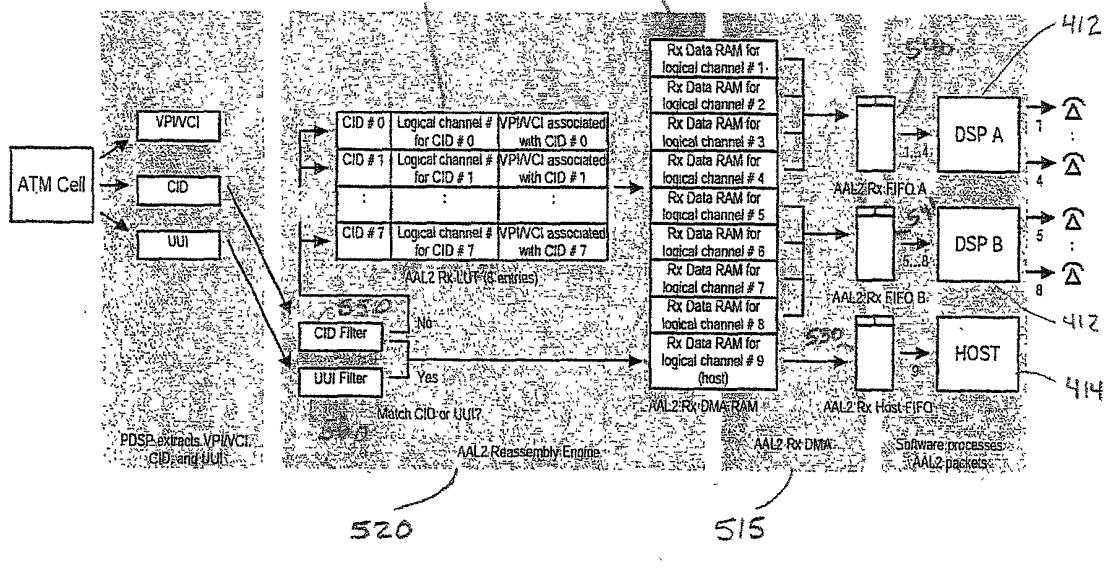


Figure 5

56-byte Aligned ATM cell

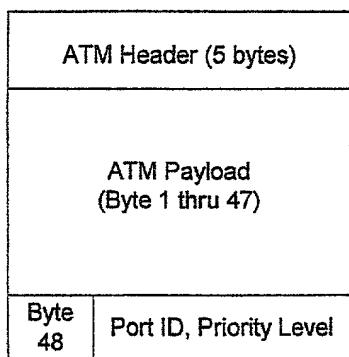


Figure 7

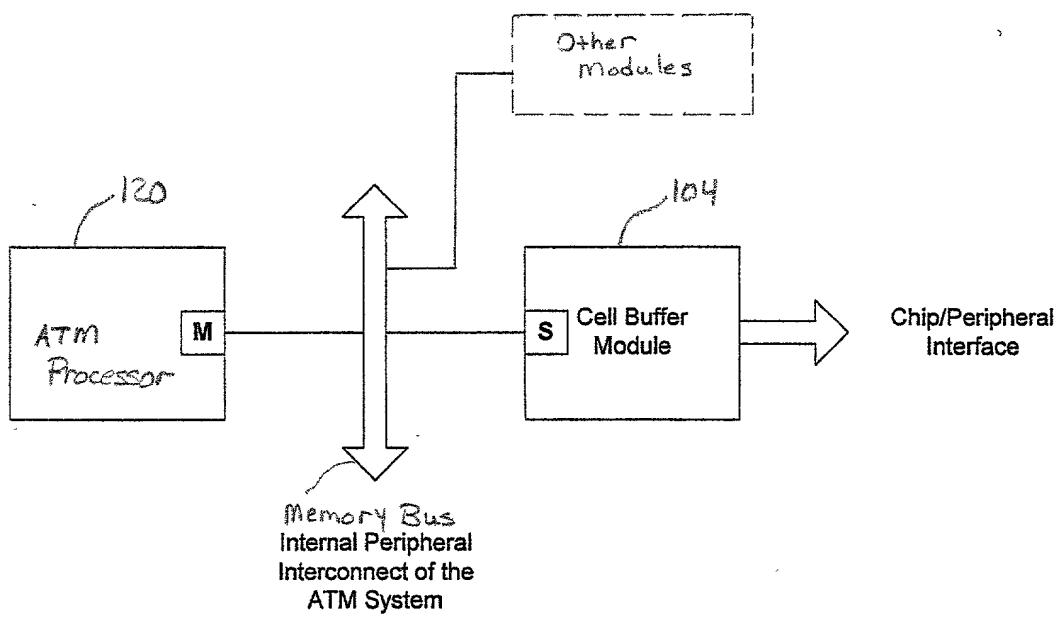


Figure 6 A

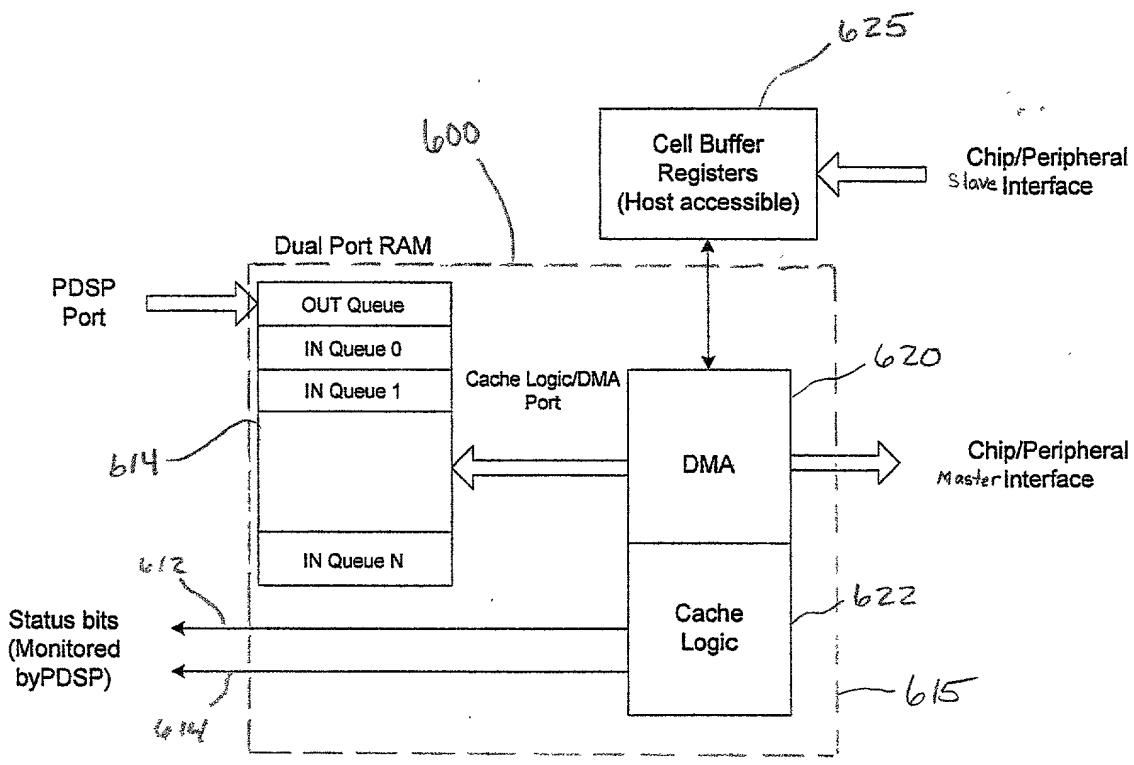


Figure 6B

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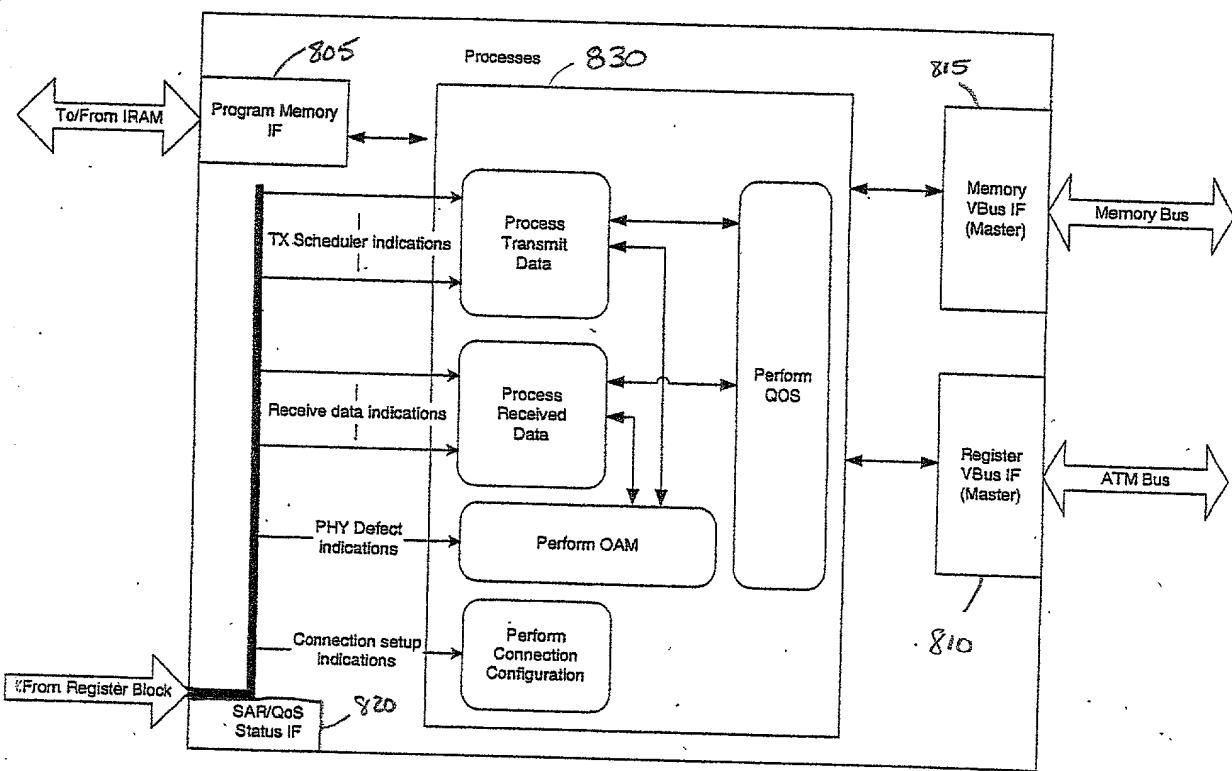


Figure 8